

| L Number | Hits | Search Text | DB | Time stamp |
|----------|------|---|--|---------------------|
| 1 | 1115 | (bus adj switch) and memory | USPAT; EPO; JPO; DERWENT; USOCR | 2003/06/07 14:10 |
| 2 | 26 | ((bus adj switch) and memory) and (transfer adj gate) | USPAT; EPO; JPO; DERWENT; USOCR | 2003/06/07 14:11 |
| 3 | 110 | ((bus adj switch) and memory) and (input adj buffer) | USPAT; EPO; JPO; DERWENT; USOCR | 2003/06/07 14:12 |
| 4 | 9 | ((bus adj switch) and memory) and (input adj buffer)) and (chip adj select) | USPAT; EPO; JPO; DERWENT; USOCR | 2003/06/07 14:14 |
| 5 | 15 | ((bus adj switch) and memory) and (input adj buffer)) and (input adj terminal) | USPAT; EPO; JPO; DERWENT; USOCR | 2003/06/07 14:18 |
| 6 | 90 | ((bus adj switch) and memory) and (input adj buffer)) and (data adj bus) | USPAT; EPO; JPO; DERWENT; USOCR | 2003/06/07 14:18 |
| 7 | 0 | ((bus adj switch) and memory) and (input adj buffer)) and (data adj bus)) and dacoder | USPAT; EPO; JPO; DERWENT; USOCR | 2003/06/07 14:18 |
| 8 | 72 | ((bus adj switch) and memory) and (input adj buffer)) and (data adj bus)) and decoder | USPAT; EPO; JPO; DERWENT; USOCR | 2003/06/07 14:19 |
| 9 | 65 | ((bus adj switch) and memory) and (input adj buffer)) and (data adj bus)) and decoder) and register | USPAT; EPO; JPO; DERWENT; USOCR | 2003/06/07 14:19 |